

1 FW 2115

PTO/SB/21 (09-04)

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Application Number	09/978,357
Filing Date	10/16/2001
First Named Inventor	Carpenter
Art Unit	2115
Examiner Name	Mark CONNOLLY

Attorney Docket Number

AUS920010957US1 (IBM 2364000)

ENCLOSURES (Check all that apply)

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Remarks		
Reply to Office Action dated November 4, 2004 (13 pages)		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	CARR LLP		
Signature			
Printed name	Theodore F. Shiells		
Date	2/4/05	Reg. No.	31.569

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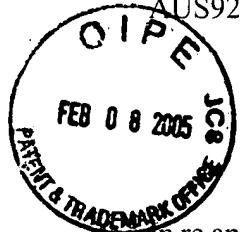
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~~AUS920010957US1~~ (IBM 2364000)

PATENT APPLICATION
SERIAL NO. 09/978,357



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Carpenter et al.

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Serial Number: 09/978,357

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Filed: October 16, 2001

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For: METHOD OF SYNCHRONIZATION
BETWEEN HIGH SPEED PROCESSOR
DEVICE CONFIGURATION INTERFACE
AND LOW SPEED DEVICES

Group Art Unit: 2115

Examiner: Mark Connolly

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RESPONSE

Dear Sir:

In response to the Office Action dated November 4, 2004 (“Office Action”), Applicants respond as follows: